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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	Art Unit:	2827	
Tyler A. Lowrey et al.	§	Examiner:	Thong Quoc Le	
Serial No.:	10/633,872	§	Docket:	ITO.0046US
Filed:	August 4, 2003	§	P16201	
For:	Read Bias Scheme for Phase Change Memories	§	Assignee:	Intel Corporation
		§		

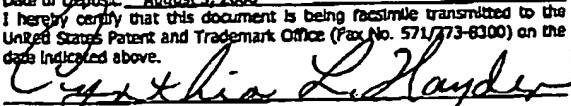
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P.O. Box 1450  
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed July 20, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: <u>August 3, 2006</u> I hereby certify that this document is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 571/273-6300) on the date indicated above.   Cynthia L. Hayden
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